received by the power switch to regulate a power supply output.

(Opm)

(Amended) The power supply controller of claim 21 wherein the current is representative of a feedback signal from the power supply output, wherein a power supply voltage is regulated through current limit adjustment of the power switch in response to the feedback signal

Br

34. (Amended) The power supply controller of claim 33 wherein the control circuit is to generate said switching waveform controlled in response to the output of the first comparator such that the switching waveform is coupled to limit the current through the power switch.

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(Amended) The power supply controller circuit of claim 25 wherein the current limit of the current through the power switch is fixed at the predetermined maximum level for magnitudes of the currents that are higher than the current value corresponding to the predetermined maximum level.

REMARKS

Claims pending in the instant application are numbered 29-42 and 64-78. Claims 31-34 and 36 are rejected. The Applicants note with appreciation that claims 29, 30, 35-42 and 64-78 are allowed. Claims 31, 32, 34 and 36 are amended. The Applicants respectfully request that the instant application be reconsidered in view of the amendments and the remarks.

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Examiner: Han, Y.